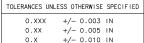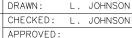1.894

3.150

1.320

1.496

1.615

0.429

NOTES:

—CONNECT +8–28V POWER TO J1 AS SHOWN ON PCB SILK–SCREEN.

—J1 IS PROTECTED FROM ERRANT CONNECTIONS, HOWEVER VOLTAGES ABOVE 30V WILL DAMAGE THE DEVICE.

—SW1 IS A RESET SWITCH, AND IS INTENDED FOR USE IN TESTING AND CONFIGURING INSTALLATIONS.

—SW2 IS A TEST SWITCH, AND IS INTENDED FOR USE IN TESTING AND CONFIGURING INSTALLATIONS. PRESSING SW2 WILL FORCE A DIAL–OUT.

—PRESSING AND SW2 AND PRESSING AND RELEASING SW1 AT THE SAME TIME WILL ENTER PROGRAMMING MODE. INSTRUCTIONS FOR PROGRAMMING ARE SHOWN BELOW.

—TO CONFIGURE THE AUTODIALER:
   —REMOVE CONNECTOR FROM J3;
   —INSTALL JUMPER ON JP1;
   —CONNECT J2 TO A PC USING A STANDARD SERIAL CABLE;
   —USE HYPERTERMINAL CONFIGURED FOR:
      —9,600bps *,
      —8–DATA BITS, NO PARITY, 1 STOP BIT,
      —HARDWARE (RTS/CTS) FLOW CONTROL;
   —HOLD DOWN SW2;
   —PRESS AND RELEASE SW1;
   —RELEASE SW2;
   —FOLLOW THE PROMPTS;
   —AFTER RESTARTING, REMOVE JUMPER FROM JP1.

* IF BAUD RATE HAS BEEN CHANGED FROM THE DEFAULT (9,600bps) THE TERMINAL PROGRAM WILL NEED TO MATCH THE NEW RATE. SEE USER'S MANUAL FOR FURTHER DETAILS.

—MOUNTING METHOD IS A UNIVERSAL DIN–RAIL MOUNT, THREE DIFFERENT RAILS ARE SHOWN.

TOLERANCES UNLESS OTHERWISE SPECIFIED		
0.XXX	+/− 0.003 IN	
0.XX	+/− 0.005 IN	
0.X	+/− 0.010 IN	

DRAWN:	L. JOHNSON
CHECKED:	L. JOHNSON
APPROVED:	

PART No. ADLR	REV 1.1A
DWG No: ADLRM31121	SIZE A

TITLE: AUTODIALER MODULE

SHEET 1 OF 1   DATE: 2005.10.27